#### CHAPTER 1 GENERAL DESCRIPTION

#### 1.1 Introduction

Chapter 1 gives general information and specifications.

Chapter 2 contains all necessary information for installing.

Chapter 3 informs about the necessary maintenance.

Chapter 4 informs about programming.

Chapter 5 contains all electrical drawings.

Chapter 6 deals with "engeneering change orders" (ECO's).

Appendix 1 standard address and vector prom list.

# 1.2 Purpose

DTC 4S is a dual width card, that plugs directly into the LSI-11 Q-bus.

The card contains 4 independent asynchronious serial line interfaces.

Each channel has its own EIA RS232 interface, with selection of data rate, data format, Q-bus address and interrupt vector address.

The DTC4S connects to external devices through a 40 pin connector. At the end of this cable there's a connector—assembly which provides four 25 pin D-type connector, switches for data—rates and jumpers. (for setting see section 3). The connector assembly is called RS3000, and is standard included with the DTC4S.

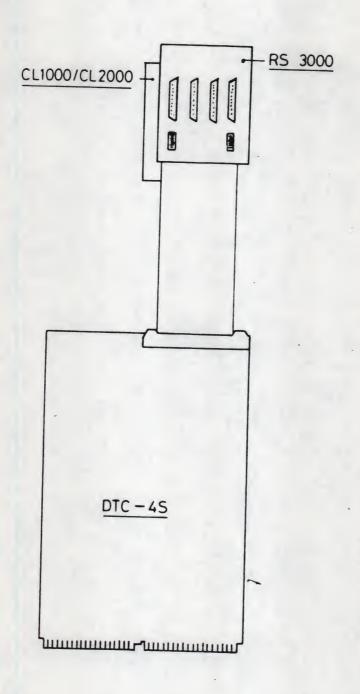
As an option, there are two types of current loop adaptors:
- CL1000 four channels 20 mA current loop, up to 9600 baud.
- CL2000 four channels 20 mA current loop, up to 19200 baud.

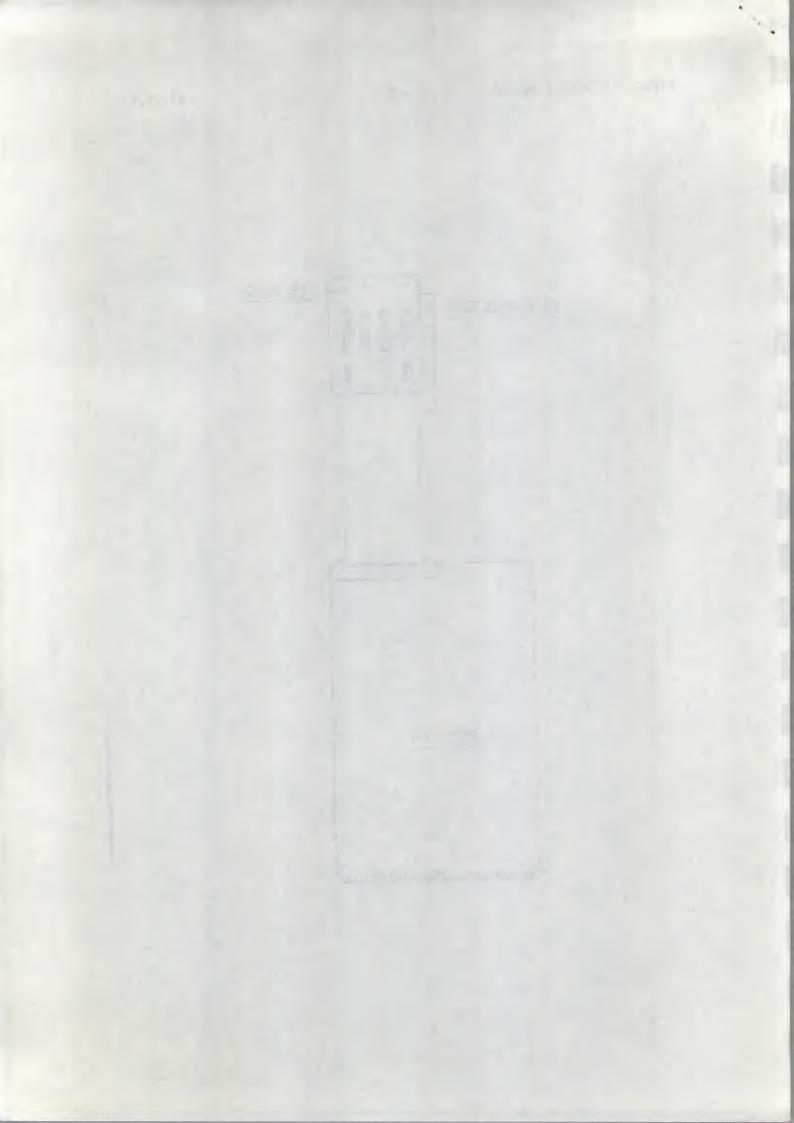
Both are jumper selectable for active or passive operation. The option can be plugged easely onto the RS3000 assembly.

If current loop option is installed, RS232 as well as CL is aviable for transmitted data.

Received data can be jumpered per channel for RS or CL (RS3000 brd).

When the currentloop adaptor is strapped as passive, the adaptor provides a galvanic insulation between terminal and interface.





#### 1.3 Specifications

#### 1.3.1 Physical dimensions

PCB thickness width

: 1.6 mm : 133 mm

Length

: 229 mm (including plastic handles)

max comp. height : 10 mm

total thickness : 12.5 mm

#### 1.3.2 Operating environment \_\_\_\_\_

Temperature : ambient air temperature range of 0 to +55 degrees C

Thermal shock: 30 degrees C per hour

Humidity : 0 to 95% relative humidity (non condensing)

Cooling : Suggested air flow of 25 cfm

## 1.3.3 Shipping environment

: the DTC4S can withstand a temperature Temperature

range of -40 to +85 degrees C during shipping

and storage.

Thermal shock

: 10 degrees C per minute.

Mechanical shock : The DTC4S module housed in its shipping-

container can withstand a mechanical shock resulting of a drop conforming to test in accordance with MIL-STD-810B, method 516,

procedure V without any damage or degradation.

# 1.3.4 Power requirements

	-	without adaptor	rs232	current loop	
+ 5V +12V		0.8A 0.08A	0.9A 0.1A	1.1A 0.4A	

Internal generation of -12V

## 1.3.5 Adressing and speed range

174000-177770 Addressing range: (prom selectable) 0-774 Vector range: (prom selectable)

50,75,110,134.5,150,300,600,1200,1800,2000 Speed selection:

2400,3600,4800,7200,9600,19200 baud.

(jumper or switch or remote selectable see 3.4)

## The condition is be made by considerable interest 1.3.6 Reliability

The DTC4S modules are designed to meet the best commercial standards of workmanship. Extensive testing is conducted to ensure a reliable service over the products' lifetime.

#### 1.4 Options

As an option there are two types of current loop adaptors:
- CL1000, four channel 20 mA current loop, up to 9600 baud.
- CL2000, four channel 20 mA current loop, up to 19200 baud.

Both are jumper selectable for active or passive operation. The option can be plugged easely onto the RS3000 assembly.

If current loop option is installed, RS232 as well as CL is aviable for transmitted data.

Received data can be jumpered per channel for RS or CL Jumper position q and k on the RS3000 board.

## CHAPTER 2 INSTALLATION

The section contains information on procedures to install DTC4S modules in Q-bus computer systems.

Installation should be performed by qualified personnel only. Incorrect installation cannot only damage the interface board, but also all other system components.

After carefully unpacking the interface board, it should be inspected for any physical damage. IF any damage is found or expected, contact your distributor and do not install the board.

## 2.1 Switches/Jumpers DTC4S

## 2.1.1 Address selection

The address selection is performed by inserting the correct prom, with programmed pattern, into the prom-position marked as Ax. The addresses of all channels have to be within the range of 174000-177770. Appendix 1 shows the standard address selections. Other combinations can be made by Datelcare on customer request.

# 2.1.2 Vector selection

The vector selection is performed by inserting the correct prom, with programmed pattern, into the prom-position marked as Cx. The vectors of all channels have to be within the range of 000-774. Appendix 1 shows the standard vector selections. Other combinations can be made by Datelcare on customer request.

#### 2.4 Pin-out D-type connector

Factory pin out, (terminal setting). Type of connector DB25-S (female).

pin NC 2 Transmit data RS232 3 Receive data RS232 5 Clear to send, fixed at +12V Data set ready, fixed at +12V 6 7 Signal ground 8 Carier detect, Fixed at +12V 13 Remote baud rate DR A Remote baud rate 14 DR B DR C 15 Remote baud rate 16 DR D Remote baud rate 20 NC CL in + 17 24' CL in -23 CL out + 25 CL out -

Modem setting (see 2.2).

Type of connector DB25-S (female).

pin NC 2 Receive data RS232 3 Transmit data RS232 4 Request to send fixed at +12V 5 NC NC 7 Signal ground Carier detect, Fixed at +12V 13 DR A Remote baud rate 14 DR B Remote baud rate 15 DR C Remote baud rate 16 DR D Remote baud rate 20 Data terminal ready, fixed at +12V

#### 2.5 Required tools and materials

No special tool or materials are necessary to install the memory board. A sharp instrument can be used to set the switches. A hand wire-wrap tool is used to strap or unstrap the jumpers.

#### 2.6 Adjustments

No adjustments are necessary.

# CHAPTER 3 MAINTENANCE

# 3.1 Regular inspection

The DTC4S board does not need any special maintenance, other than normally performed with computer systems.

Twice a year the board should be removed from the system. The Q-bus edge connector should be inspected for dust, corrosion or other contaminents and if necessary, cleaned with alcohol (Ethanol 99%) and a soft cloth.

#### 3.2 Hardware testing

The hardware testing is done by installing the interface in the computer system and running the appropriate diagnostic software (see 3.3).

## 3.3 Software testing

With the interface board no software is delivered. DEC test VKAE can be used to test the module.

#### CHAPTER 4 PROGRAMMING

Bit(s)

#### 4.1 Register definition

Since each serial channel has four registers, each requires four addresses. The "BASE" address corresponds to the Receive/Status Register (RCSR). The addresses of the other registers of the channel then default as follows:

Base	RCSR	(Receive Command/Status Register)
Base+2	RDBR	(Receive Data Buffer Register)
Base+4	XCSR	(Transmitter Command/Status Register)
Base+6	XDBR	(Transmitter Data Buffer Register)

#### RCSR - Receiver Command/Status Register

**Function** 

15-08	Not used. Read as O.
07	Receiver Done. Set whwn an entire character has been received and is ready for input to the processor.
	This bit is automatically cleared when RDBR is addressed or when the BDCOK H signal goes low.
	A receiver interrupt is requested by the channel

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when this bit is set and receiver interrupt is enabled (bit 6 is also set). Read only bit.

Interrupt Enable. Set under program control when it is desired to generate a receiver interrupt request when a character is ready for input to the processor (bit 7 is set). Cleared under program control or by the BINIT signal. Read/Write bit.

05-00 Not used. Read as 0.

# RDBR - Receiver Data Buffer Register

15-08 Not used. Read as 0.

07-00 Contains five to eight data bits in a right-justified format. The most significant bit (MSB) is the optional parity bit. The parity bit is not available if 8 bit/char data is selected. Read-only bit.

# XCSR - Transmitter Command/Status Register

15-08 Not used. Read as 0.

Transmit Ready. Set when XDBR is empty and can accept another character for transmission. It is also set during the power-up sequence by the BDCOK H signal. This bit is automatically cleared when XDBR is loaded. A receiver interrupt is requested by the channel when this bit is set and receiver interrupt is enabled (bit 6 is also set). Read only bit.

Interrupt Enable. Set under program control when it is desired to generate a transmitter interrupt request when the channel is ready to accept a character for transmission (bit 7 is set). Cleared under program control or by the BINIT signal. Read/Write bit.

05-01 Not used. Read as 0.

OD Break. Set or reset under program control. When set, a continuous space level is transmitted. BINIT resets this bit. Read/Write bit.

# XDBR - Transmitter Data Buffer Register

15-08 Not used. Read as 0.

07-00 Contains five to eight data bits in a right-justified data bits. Loaded under program control for serial transmission to a device. Parity, if enabled, is automatically generated and suffixed to the data as it is transmitted. Write only, always read as 0.